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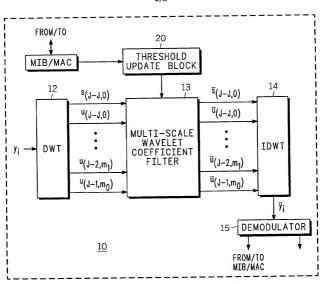
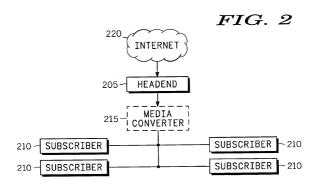


FIG. 1



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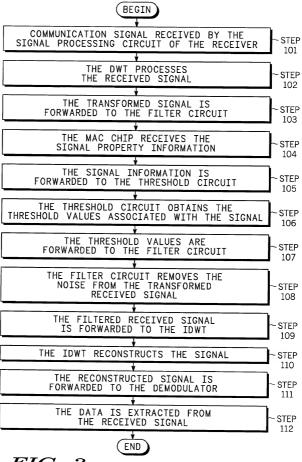
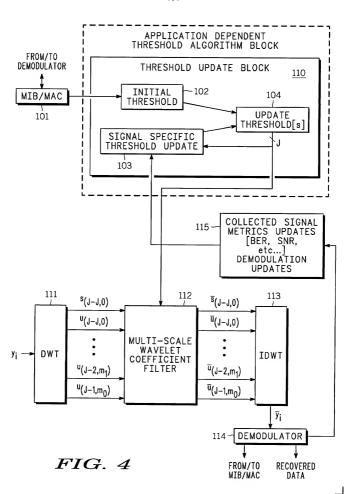


FIG. 3



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BEGIN COMMUNICATION SIGNAL RECEIVED BY THE STEP SIGNAL PROCESSING CIRCUIT OF THE RECEIVER 501 THE DWT PROCESSES STEP THE RECEIVED SIGNAL 502 THE TRANSFORMED SIGNAL IS STEP FORWARDED TO THE FILTER CIRCUIT 503 THE MAC CHIP RECEIVES THE STFP SIGNAL PROPERTY INFORMATION 504 THE SIGNAL INFORMATION IS STFP FORWARDED TO THE THRESHOLD CIRCUIT 505 THE THRESHOLD CIRCUIT OBTAINS THE STFP THRESHOLD VALUES ASSOCIATED WITH THE SIGNAL 506 THE THRESHOLD VALUES ARE STFP FORWARDED TO THE FILTER CIRCUIT 507 THE FILTER CIRCUIT REMOVES THE NOISE FROM THE TRANSFORMED STEP RECEIVED SIGNAL 508 THE FILTERED RECEIVED SIGNAL STEP IS FORWARDED TO THE IDWT 509 THE IDWT RECONSTRUCTS THE SIGNAL - STFP 510 THE RECONSTRUCTED SIGNAL IS STFP FORWARDED TO THE DEMODULATOR 511 THE COLLECTOR CALCULATES STEP THE DEMODULATOR PROPERTIES 512 FIG. 5

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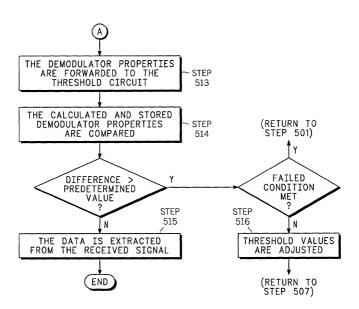


FIG. 6